



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#11 Suppl IDS

Applicant : Kusumoto, et al. Art Unit : 2814
Serial No. : 09/941,367 Examiner : Theresa T. Doan
Filed : August 28, 2001
Title : METHOD FOR PRODUCING INSULATED GATE THIN FILM
SEMICONDUCTOR DEVICE

M. Benson
1/2/03

Commissioner for Patents
Washington, D.C. 20231

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and document listed on form PTO-1449.

This filing is being made before the receipt of a first Office action on the merits. No fee is required.

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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Respectfully submitted,



Scott C. Harris
Reg. No. 32,030

KENYON S. JENCKES
REG. NO 41,873

Fish & Richardson P.C.
PTO Customer No. 20985 * [REDACTED] *
4350 La Jolla Village Drive, Suite 500
San Diego, CA 92122
Telephone: (858) 678-5070
Facsimile: (858) 678-5099

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